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# China Electronic Component Center Laboratory Test Report

Part Number : S9S122F0VLH

Manufacturer : NXP
Date Code : /

Package : LQFP-64

Quantity Submitted : 10 pieces

Quantity Tested : 10 pieces

Date of Received : 2021/07/28

Date of Tested : 2021/07/29

\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

#### **General Conclusion**

10 pieces of NXP <u>S9S12G192F0VLH</u> were received for analysis.

#### 1. External Visual Inspection

10 pieces samples were used for visual inspection. The inspected samples show clear top marking. Part failed marking permanency test. Samples also show different texture on sidewall, indicating the parts were top-coated. Sample exhibit oxidation, attrition, dents on pins.

lpiece sample was performed package dimensions testing. The tested dimensions were within manufacture specification. The device has the same exterior configuration as shown on datasheet.

#### 3. X-Ray inspection

10 pieces samples were performed X-Ray inspection, 9 pieces samples indicate homogeneous general shape and internal construction, the rest 1 is different from others.

#### 4. Key Functional Test

10 pieces were performed key functional test, parts passed program test, verification test, erase test and blank-check test.

The parts are classified refurbished.



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Approved by: Chen Tian Technical supervisor: Peng Huanyuan

Engineer: Zou Jianjun Report reviewer: Zeng Lirong

Project to undertake (unit): China Electronic Component Center Laboratory

Note: This report refers only to the specimen(s) submitted to test, and is invalid if used separately.



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### I. Test Purpose

To verify the devices according to customer's requirements.

#### **II. Test Environment**

Environment Temperature: (25±2)°C;

Environment Humidity:(55±5)% RH.

# **III. Test Equipment**

In the testing process.

#### IV. Test Standard

 $\langle\!\langle CECC\text{-STANDARD}\rangle\!\rangle$  and  $\langle\!\langle JEDEC\text{-STANDARD&AS6081}\rangle\!\rangle$  .

#### V. Test Basis

《S9S12G192F0VLH datasheet》.

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# VI. Test Procedure and Analysis

# 1. Packaging Inspection

Package:	□PVC Tube☑Tray□Reel□Anti-static bags□Plastic bags☑Vacuum-sealed □Others	
Complete la	beling	Yes
Humidity in	dicator cards (HIC):	Yes
Moisture ba	rrier bag and desiccant pouches:	Yes

#### Parts as arrived

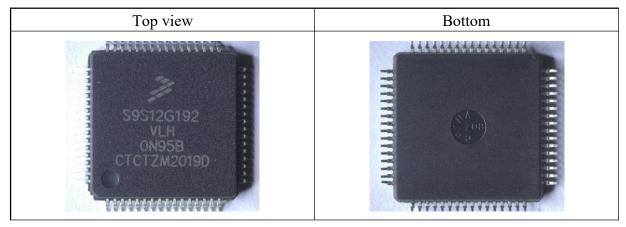








# 2. Sample appearance





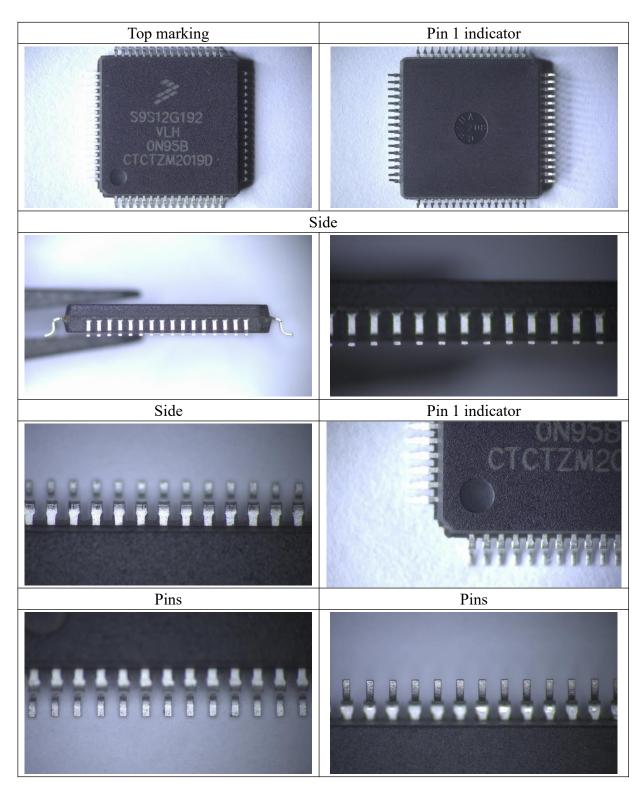
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# 3. External Visual Inspection

10 pieces devices were performed visual inspection. Test diagrams are as follows:





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Comparison	Comparison
\$9\$12G192 VLH 0N95B CTCTZM2019D (MATABLE MATABLE)	
Comparison	Comparison
Black topped	Attrition
Z. iiiiiiiiiiiiiiiiiiiiiiiiiiiiiiiiiiii	
Attrition and oxidation	Dents and attrition

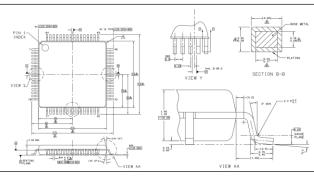


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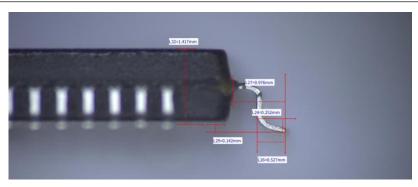
#### Dimensions



D=11.955mm, D1=9.934mm, A2=0.142mm, e=0.511mm, b=0.191mm, L1=0.976mm, A=1.553mm, A1=1.417mm, L=0.527mm, G=0.252mm







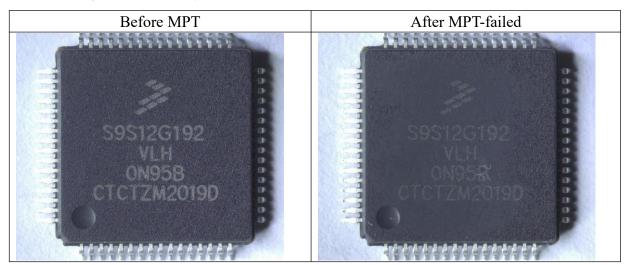


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# 4. Marking Permanency Test





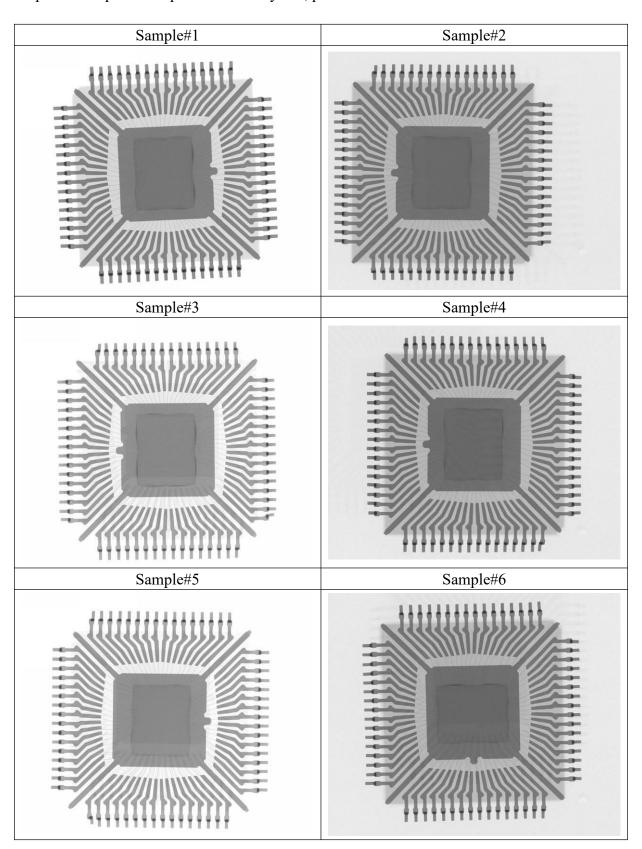
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# 5. X-Ray Test

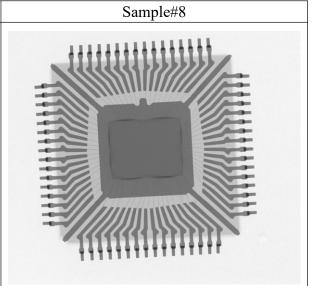
10 pieces samples were performed X-ray test, parts indicate same structure.



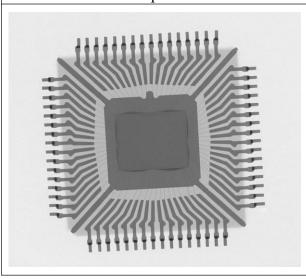


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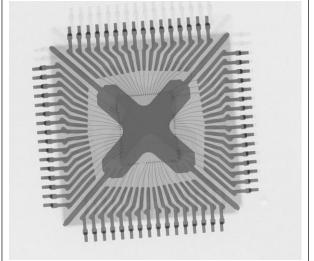
# Sample#7



Sample#9



Sample#10





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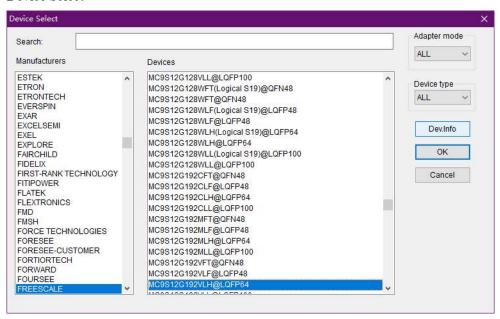
#### **6. Key Functional Test**

10 pieces were performed key functional test, with all passed.

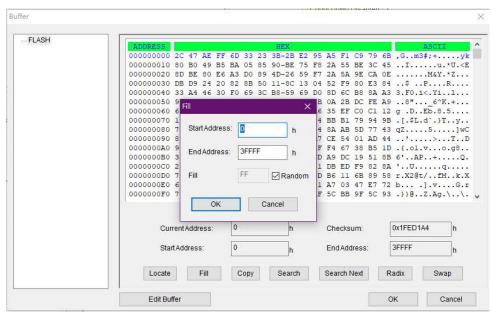
Test diagrams are as follows:

#### **6.1 Test result**

Device Select



#### Random fill buffer



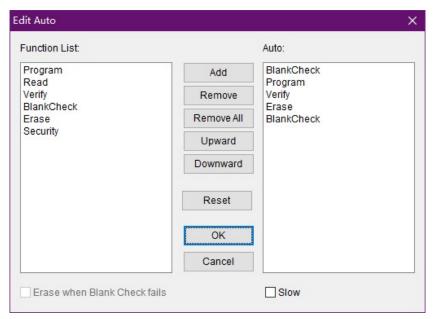


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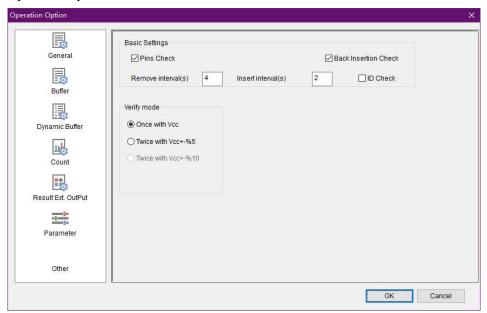
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#### Edit Auto



#### Operation Option



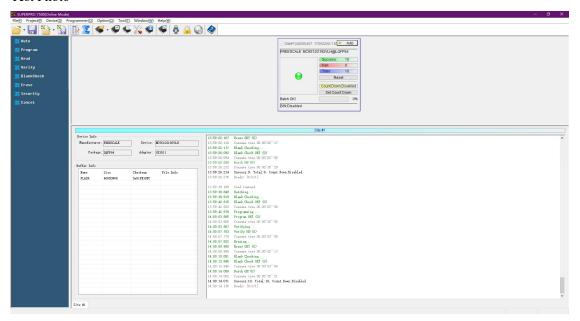


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#### Test Photo





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#### **Appendix**

Optional items:

✓ Visual Inspection

✓ X-Ray Inspection

☐ Chemical Decapsulation

□Failure Analysis

□Reliability Analysis

□Solderability Analysis

□Level I Authenticity Inspection and Verification (AIV)

□Level II Direct Current Characteristics testing (DCCT)

✓ Level III Key Functional Testing (KFT)

□Level IV Full Functional Characteristics Testing (FFCT)

□Level V Alternating Current Characteristics testing (ACCT)

□Other Items

Test Items Description Reference: <a href="http://www.cecclab.com/en/icmenu.asp?id=101&id1=101">http://www.cecclab.com/en/icmenu.asp?id=101&id1=101</a>

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#### **Brief introduction of CECC**

China Electronic Component Center Lab abbreviated as CECC, with the development of more than 15 years, we are setting up 4 engineering centers for semiconductor innovation and 2 factories for batch IC testing in Shenzhen, Xi'an, Wuxi and Jinan. CECC has been established the pleasure and firm cooperation with IC design house, distributor, EMS, etc.

#### **CECCLab WeChat ID**

